

Form PTO-1449

INFORMATION DISCLOSURE CITATION  
IN AN APPLICATION

FEB 17 2004

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Document Number (Optional)

TSMC-01-1674

Application Number

10/706,380

Applicant

Cliff Hou et al.

Filing Date

11/12/03

Group Art Unit

2825

U. S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
PD	6311314	10/30/01	McBride	716	6	4/27/99
	6053950	4/25/00	Shinagawa	716	2	2/13/98
	6020774	2/1/00	Chiu et al.	327	295	7/23/98
	5864487	1/24/99	Merryman et al.	364	491	11/19/96
	5686845	4/11/97	Erdal et al.	326	93	8/28/96
PD	6473890	10/29/02	Yasui et al.	716	10	9/18/00

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

PD -	"Clock Generation and Distribution for the First IA-64 Microprocessor," Tam et al. IEEE Jnl of Solid-State Circuits, pp. 1545-1552, Nov. 2000, Vol. 35, No. 11, ISSN: 0018-9200.
PD -	"Performance Optimization of VLSI Interconnect Layout," Cong et al., The Jnl of VLSI Integration, Vol. 21, Nos. 1+2, Nov. 1996, pp. 1-99.

EXAMINER

PAUL DINH

DATE CONSIDERED

5/5/05

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

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## U. S. PATENT DOCUMENTS

[illegible]

## FOREIGN PATENT DOCUMENTS

[illegible]

OTHER DOCUMENTS (Including Author, Title, Date, Portmox Pages, Etc.)

PD -	"An Algorithm for Zero-Skew Clock Tree Routing with Buffer Insertion," Chen et al., Proceeding-European Design and Test Conf., pp. 1-22, 1996.
PD -	"Physical Design CAD in Deep Sub-micron Era," Mitsubishi et al., Proceedings of the European Design Automation Conf. w/EURO-VHDL'96, Geneva, Switzerland, IEEE Comp. Soc. Press, CA, 1996.
EXAMINER	PAUL DINH
DATE COMPLETED	5/5/05

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## U. S. PATENT DOCUMENTS

[illegible]

## FOREIGN PATENT DOCUMENTS

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OTHER DOCUMENTS (Including Author, Title, Date, Portion of Pages, Etc.)

PD -	"Wire Segmenting for Improved Buffer Insertion," Alper et al., Proc. of the 34th Annual ACM/IEEE Design Automation Conf., 1997, ACM Press, NY, USA, pp. 588-593, ISBN: 0-89791-920-3.
PD -	"Repeater Block Planning under Simultaneous Delay and Transition Time Constraints," Sarker et al., Proc. 2001 Euro. Design, Auto. & Test Conf., March 2001, pp. 540-544.

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